

SEP 20 2006

501.32049R00/329201392US2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Yujiro KAJIHARA *et al.*

Serial No.: 09/328,910

Filed: 9 June 1999

For: LEADFRAME SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE USING THE SAME, AND METHOD  
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2815

Examiner: J. Clark

Conf. No.: 7455

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
POB 1450  
Alexandria, Virginia 22313-1450

20 September 2006

Sir:

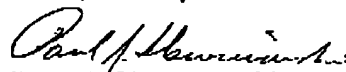
In the matter of the above-identified application, applicants hereby respectfully petition for an extension of time to permit filing a response within the third month subsequent to expiration of the shortened statutory period set in the outstanding Office Action mailed March 20, 2006.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135

(Case No. 1374.32049R00).

Respectfully submitted,

ANTONELLI, TERRY STOUT & KRAUS, LLP



Paul J. Skwierawski  
Registration No. 32,173

PJS/slk

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